SimSoC

A full system (hardware and software) simulation software for embedded systems: Development and Validation

Technological barrier: High performance hardware simulation based on Transaction Level Modeling and SystemC interfaces

Formal methods to prove system properties

Language, standard, environment: SystemC, TLM library, C++, Linux

Keywords: Instruction Set Simulation, TLM, SystemC, Dynamic Translation

License: Open Source LGPL available from http://gforge.inria.fr/projects/simsoc

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http://formes.asia/cms/software/simsoc